

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	4	(714/741.ccls.) and emulation and ((configur\$5 or program\$4) with ((logic adj block \$1) or FPGA)) and test\$3	US-PGPUB; USPAT; USOCR	OR	OFF	2008/08/07 13:14
L2	7	(714/726.ccls.) and emulation and ((configur\$5 or program\$4) with ((logic adj block \$1) or FPGA)) and test\$3	US-PGPUB; USPAT; USOCR	OR	OFF	2008/08/07 13:14
L3	0	(714/717.ccls.) and emulation and ((configur\$5 or program\$4) with ((logic adj block \$1) or FPGA)) and test\$3	US-PGPUB; USPAT; USOCR	OR	OFF	2008/08/07 13:14
L4	6	(714/738.ccls.) and emulation and ((configur\$5 or program\$4) with ((logic adj block \$1) or FPGA)) and test\$3	US-PGPUB; USPAT; USOCR	OR	OFF	2008/08/07 13:15
L5	11	(714/724.ccls.) and emulation and ((configur\$5 or program\$4) with ((logic adj block \$1) or FPGA)) and test\$3	US-PGPUB; USPAT; USOCR	OR	OFF	2008/08/07 13:15
L6	0	(714/48.ccls.) and emulation and ((configur\$5 or program\$4) with ((logic adj block \$1) or FPGA)) and test\$3	US-PGPUB; USPAT; USOCR	OR	OFF	2008/08/07 13:15

L7	48	(716/ 16.ccls.) and emulation and (((configur\$5 or program\$4) with ((logic adj block \$1) or FPGA)) and test\$3	US-PGPUB; USPAT; USOCR	OR	OFF	2008/08/07 13:15
L10	169	emulation and (((configur\$5 or program\$4) with ((logic adj block \$1) or FPGA)) same test\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/08/07 13:18
S193	14	montagne-xavier. IN.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/07/24 13:29
S196	1	(maquignon- frank.IN.)	US-PGPUB; USPAT; USOCR	OR	OFF	2008/07/24 13:44
S200	13	(703/ 13.ccls.) and emulation and (((configur\$5 or program\$4) with ((logic adj block \$1) or FPGA)) and test\$3	US-PGPUB; USPAT; USOCR	OR	OFF	2008/07/24 15:46
S201	3	(703/ 13.ccls.) and emulation and (((configur\$5 or program\$4) with ((logic adj block \$1) or FPGA)) with test\$3)	US-PGPUB; USPAT; USOCR	OR	OFF	2008/07/24 15:50
S203	8	(703/ 14.ccls.) and emulation and (((configur\$5 or program\$4) with ((logic adj block \$1) or FPGA)) with test\$3)	US-PGPUB; USPAT; USOCR	OR	OFF	2008/07/24 15:56
S204	1	output with feedback with fail \$3 with indica\$4 with (maintain\$3 or keep\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/08/04 11:04
S205	0	(output near feedback) with (fail \$3 adj indica\$4) with (maintain\$3 or keep\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/08/04 11:06

S206	8	(output near feedback) with ((fail\$3 adj indica \$4) or result) with (maintain\$3 or keep\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/08/04 11:08
S207	5	(output near feedback) with ((fail\$3 adj indica \$4) or result) with test\$3 with circuit \$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/08/04 11:39
S208	0	(output near feedback) with ((fail\$3 adj indica \$4) or result) with test\$3 with (comparator or verifier or analyzer)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/08/04 21:20
S209	11	(output near feedback) with ((fail\$3 adj indica \$4) or result) with test\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/08/04 21:21
S211	12	maintain\$3 with output with feedback with (result or indicat \$3) with test\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/08/04 21:34
S212	10	maintain\$3 with output with feedback with (result or indicat \$3) with test\$3 with circuit\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/08/04 21:35
S224	40	((Inverse or opposite or reverse) adj rout \$3) and (logic adj circuit\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/08/06 10:20
S242	8	(input adj test adj pattern) with (output adj test adj pattern) with (system or process \$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/08/06 17:24

S244	29	input with (test adj pattern\$1) with output with conver\$3 with (system or process \$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/08/06 17:29
S245	19	(logic or FPGA) with conver\$4 with input with (test adj pattern) with output	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/08/06 17:38

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